

LITE-80

作者：MCU Ecosystem Development Team

改动记录

1. 2022/10/27 V1B 初版


使用说明

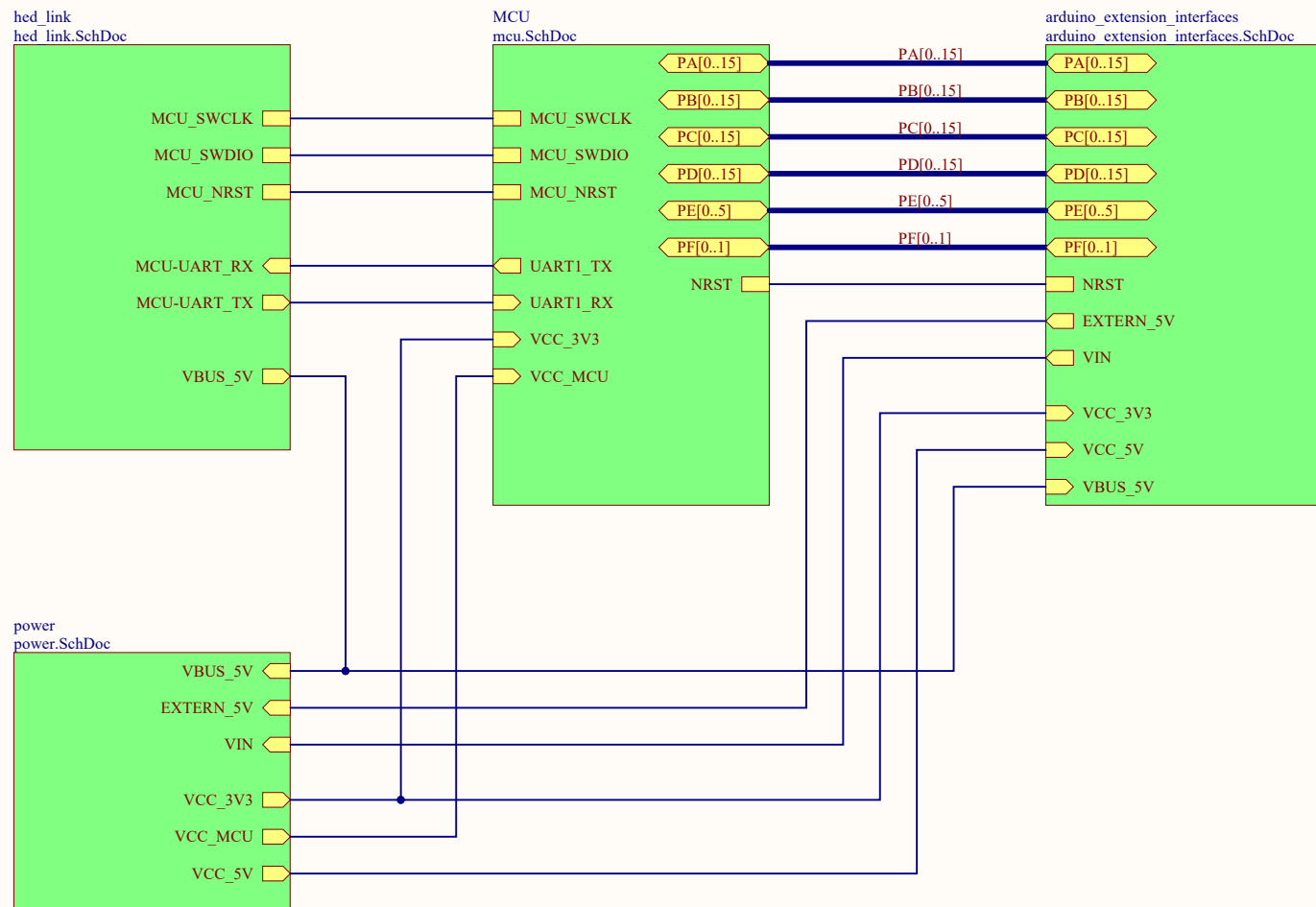
- USB接口用于程序下载与调试
1. USB接口作为HED_LINK:可用于程序下载与调试
2. USB接口作为虚拟串口只可用于程序下
- 下载前将BOOT引脚接高电平，按复位按键


TOP
hb1002b_top.SchDoc

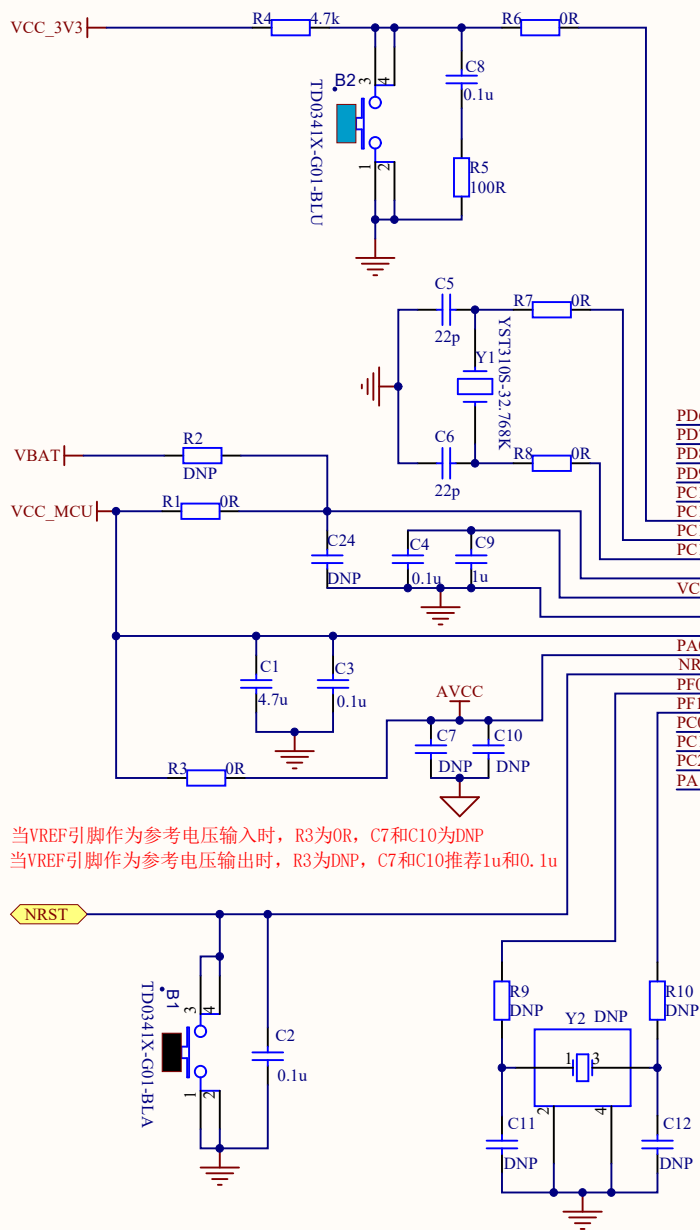


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Title: <u>project overview</u>		
Project: LITE-80		
Reference: HB1002	Rev: B	
Variant: None	Date:2022-10-27	
Size: A4	Sheet: 1 of 6	



Title: top		
Project: LITE-80		
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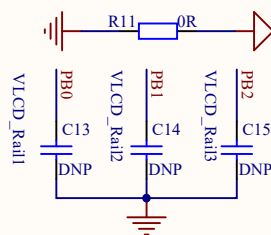
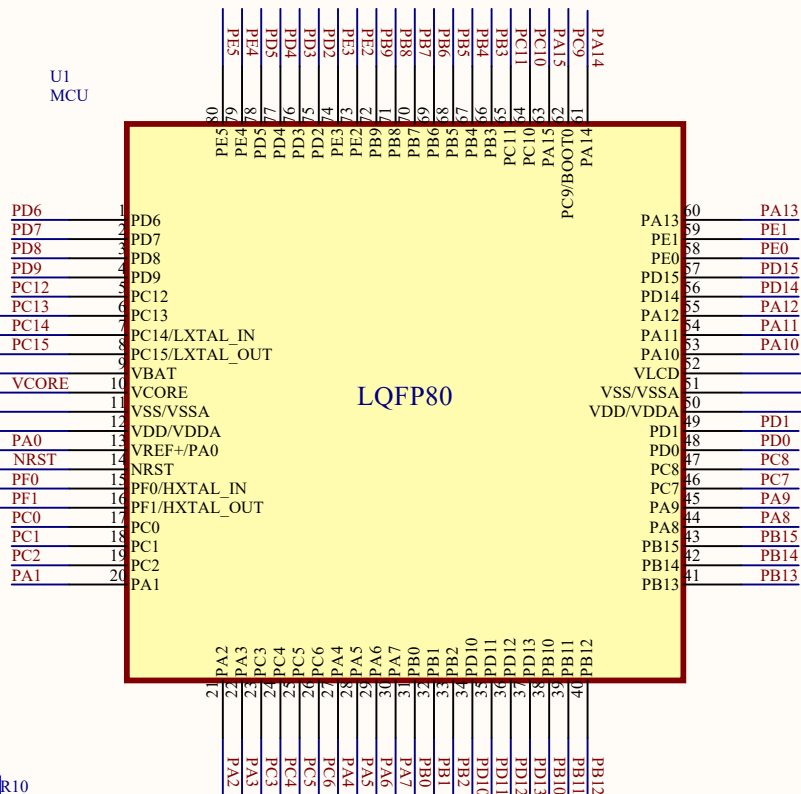


logo

注意: 电路设计时, 存在多个IO中断的情况下, 应选择在EXTI通道上的IO。

见参考手册EXTI章节EXTI IO选择寄存器。

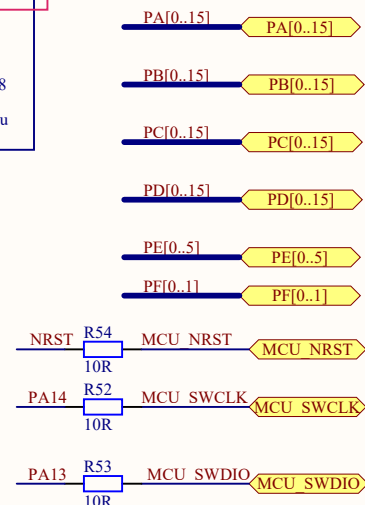
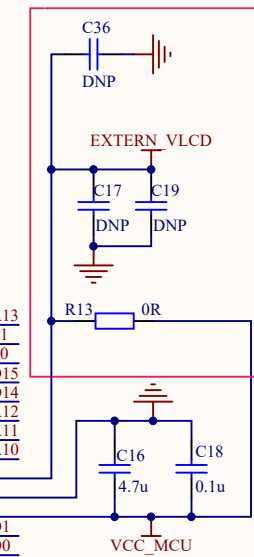
如: PA0, PB0, PC0在同一个EXTI通道上, PA1, PB1, PC1在同一个EXTI通道上。




LCD控制器电源轨去耦, 根据LCD屏选择去耦电容
推荐设置为LCD屏电容值的10倍

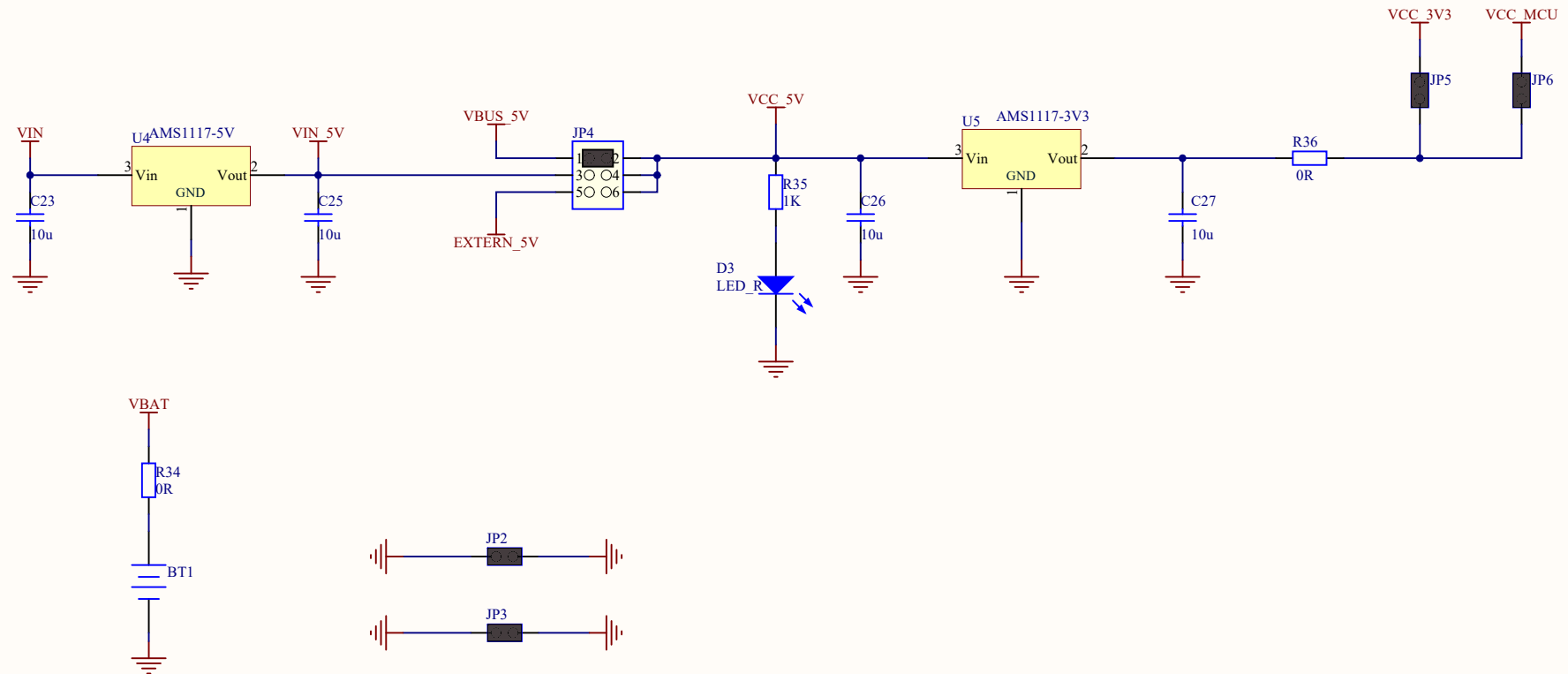
LCD控制器供电方式


- 1、采用内部电荷泵供电时, C36荐0.1uF R13、C17、C19 为DNP。
- 2、外部供电: VLCD = VCC_MCU R13为0Ω, C36、C17、C19为DNP。
- 3、外部供电: VLCD ≠ VCC_MCU C17、C19为1uF+0.1uF, C36、R13 为DNP。

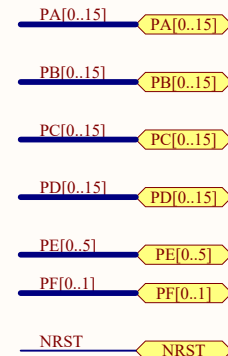
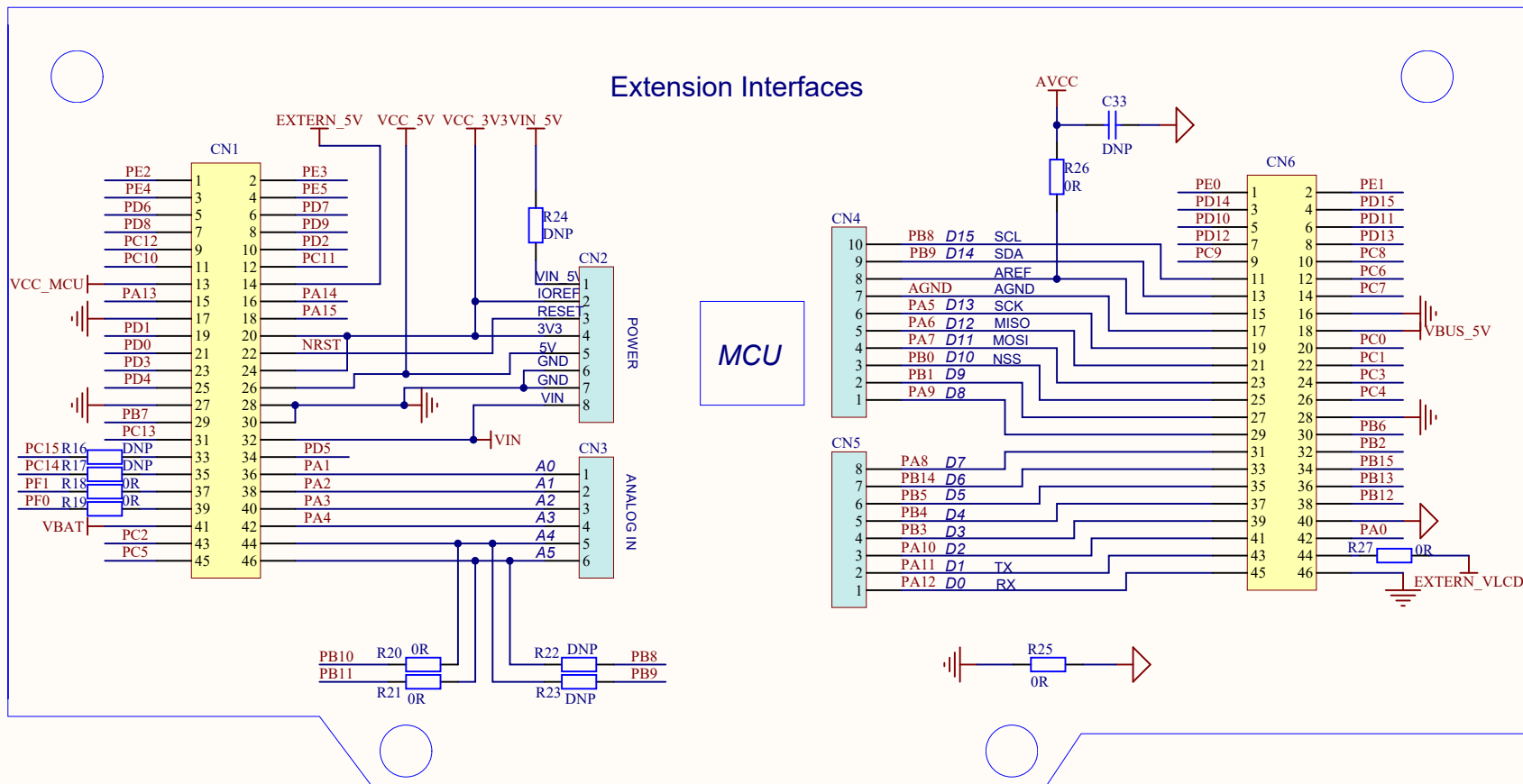



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电源电路

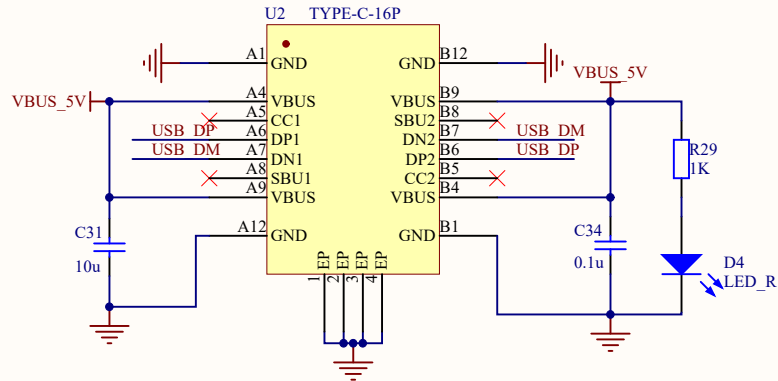


Title: power		
Project: LITE-80		
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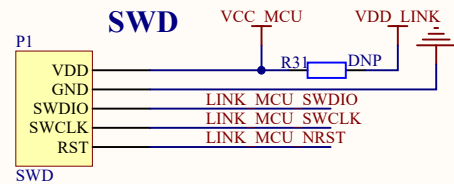
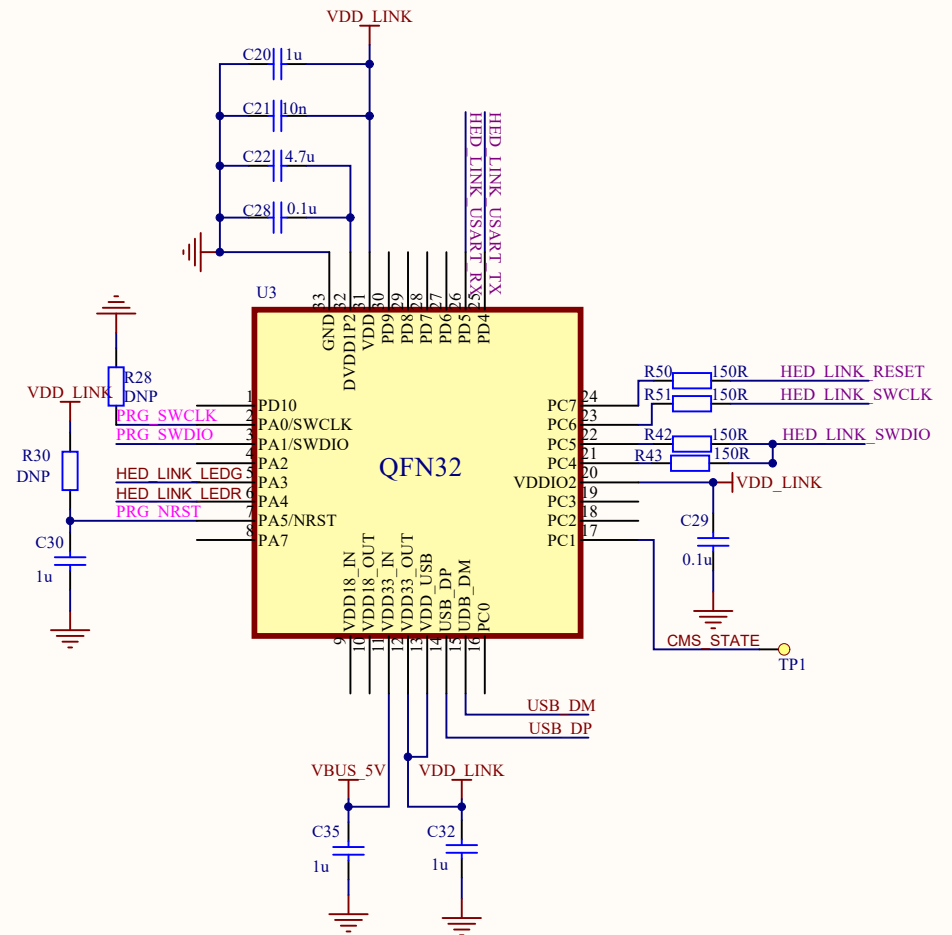
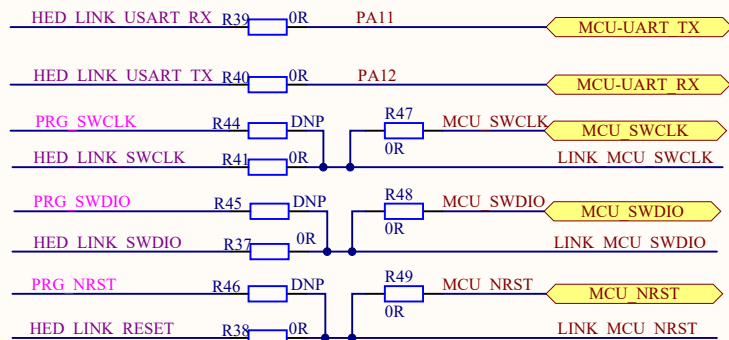
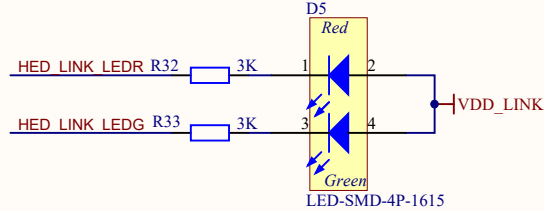



Title: arduino interface		
Project: LITE-80		
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USB TypeC



SWD

**HED_LINK**

Title:	HED Link		
Project:	LITE-80		
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